



HIGHNESSTM

HM084SV101A VER8
8.0" Color TFT-LCD

FUNCTIONAL DRAFT SPECIFICATION

(This document is meant for customers' approval)

Release Date
14th Nov 2019

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2. General Description

This specification applies to the 8.4 inch color TFT LCD module HM084SV101A VER8.

HM084SV101A VER8 designed with wide viewing angle; wide operating temperature and long life LEDs backlight is well suited to be the display units for Industrial Applications.

LED driving board for backlight unit is included in this panel and the structure of the LED units is replaceable.

HM084SV101A VER8 is built in timing controller and LVDS interface.

The screen format is intended to support the SVGA (800(H) x 600(V)) screen and 16.2M (RGB 8-bits) or 262k colors (RGB 6-bits).

HM084SV101A VER8 is a RoHS product.

2.1 Display Characteristics

The following items are characteristics summary on the table under 25 °C condition:

Items	Unit	Specifications
Screen Diagonal	[inch]	8.4 (213.4mm)
Active Area	[mm]	170.4(H) x 127.8(V)
Pixels H x V		800x3(RGB) x 600
Pixel Pitch	[mm]	0.213x 0.213
Pixel Arrangement		R.G.B. Vertical Stripe
Display Mode		TN, Normally White
Nominal Input Voltage VDD	[Volt]	3.3 typ.
Typical Power Consumption	[Watt]	4.2 typ.
Weight	[Grams]	250 \pm 10
Physical Size	[mm]	203.0(W) x 142.5(H) x 8.0(D) (typ.)
Electrical Interface		1 channel LVDS
Surface Treatment		Anti-glare, Hardness 3H
Support Color		262K(6-bit) / 16.2M(8-bit)
Temperature Range Operating Storage (Non-Operating)	[°C] [°C]	-30 to +85 (panel surface temperature) -30 to +85
RoHS Compliance		RoHS Compliance

2.2 Optical Characteristics

The optical characteristics are measured under stable conditions at 25°C (Room Temperature):

Item	Unit	Conditions	Min.	Typ.	Max.	Note
White Luminance	[cd/m ²]	I _F = 80mA (center point)	350	450	-	1
Uniformity	%	TBD	-	-	-	-
Contrast Ratio			400	600	-	2
Response Time	[msec]	Rising	-	20	30	3
	[msec]	Falling	-	10	20	
	[msec]	Raising + Falling	-	30	50	
Viewing Angle	[degree]	Horizontal (Right) CR10 (Left)	70	80	-	4
	[degree]		70	80	-	
	[degree]	Vertical (Upper) CR ≥ 10 (Lower)	65 50	80 60	- -	
Color / Chromaticity Coordinates (CIE 1931)		Red x	TBD	TBD	TBD	
		Red y	TBD	TBD	TBD	
		Green x	TBD	TBD	TBD	
		Green y	TBD	TBD	TBD	
		Blue x	TBD	TBD	TBD	
		Blue y	TBD	TBD	TBD	
		White x	0.26	0.31	0.36	
		White y	0.28	0.33	0.38	
Color Gamut	%			45	-	

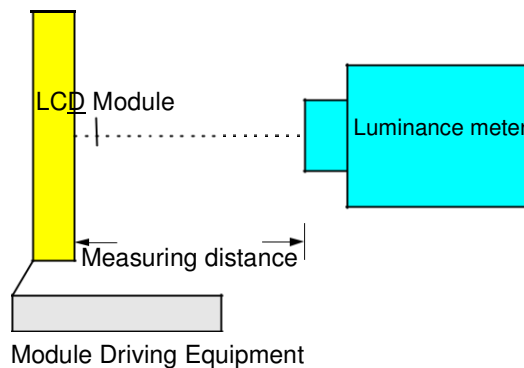
Note 1: Measurement method

Equipment : Pattern Generator, Power Supply, Digital Voltmeter, Luminance meter

Aperture 1° with 50cm viewing distance

Test Point Center

Environment < 1 lux

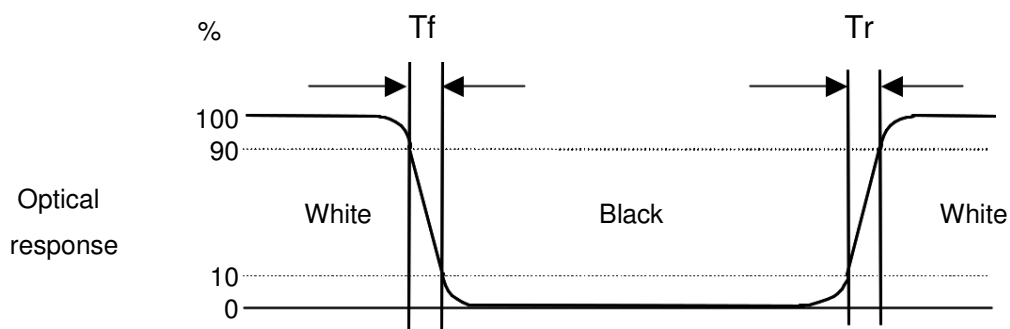


Note 2 Definition of contrast ratio (CR):

$$\text{Contrast ratio (CR)} = \frac{\text{Brightness on the "White" state}}{\text{Brightness on the "Black" state}}$$

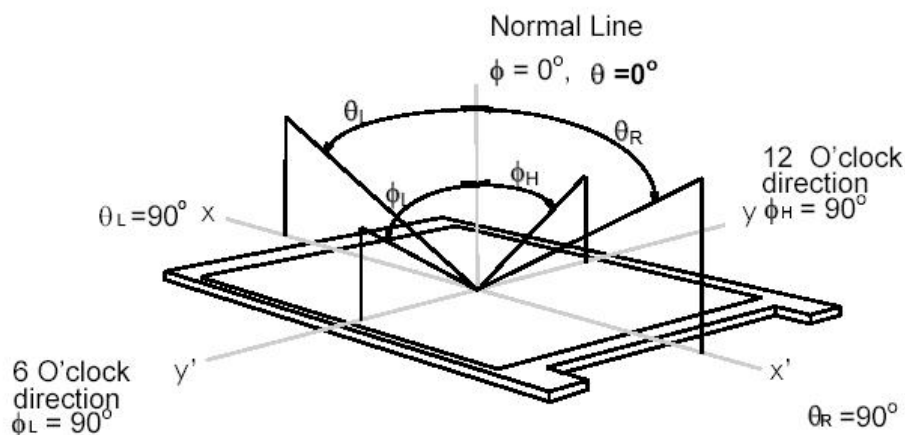
Note 3: Definition of response time:

The output signals of photo detector are measured when the input signals are changed from "White" to "Black" (falling time) and from "Black" to "White" (rising time), respectively. The response time interval is between 10% and 90% of amplitudes. Please refer to the figure as below.



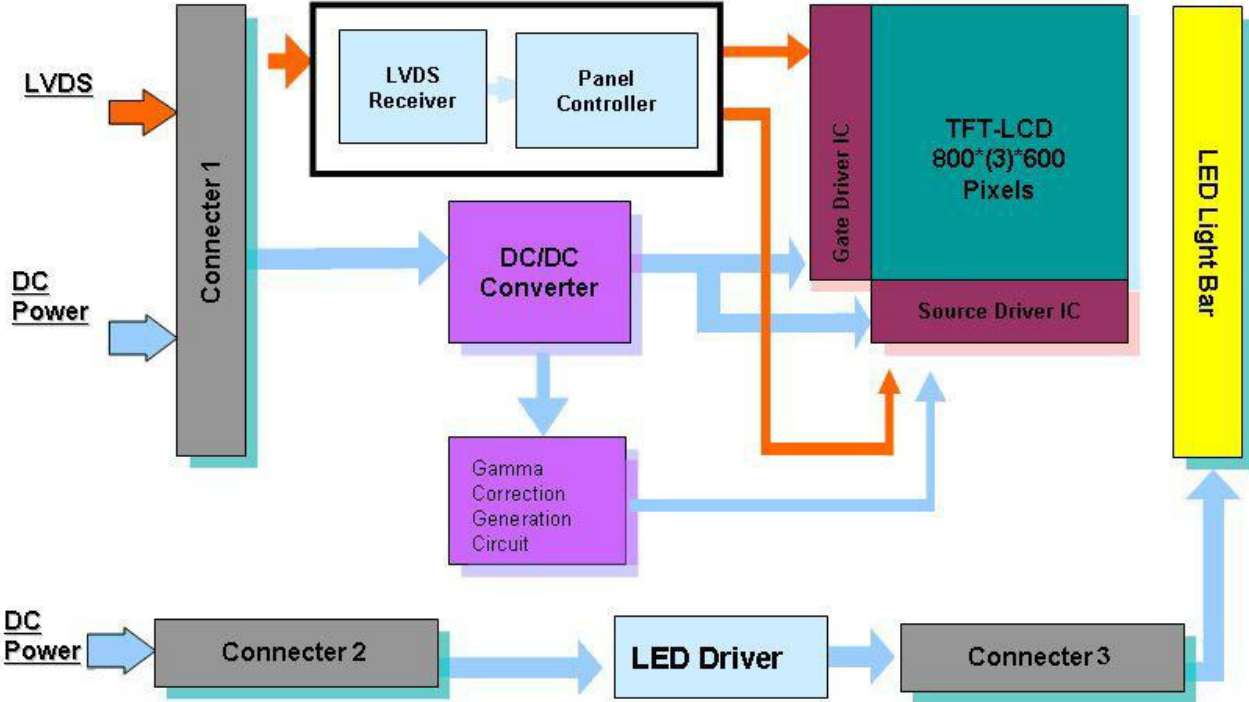
Note 4: Definition of viewing angle

Viewing angle is the measurement of contrast ratio =10, at the screen center, over a 180°horizontal and 180° vertical range (off-normal viewing angles). The 180°viewing angle range is broken down as below: 90°(θ) horizontal left and right, and 90°(Φ) vertical high (up) and low (down). The measurement direction is typically perpendicular to the display surface with the screen rotated to its center to develop the desired measurement viewing angle.



3. Functional Block Diagram

The following diagram shows the functional block of the 8.4 inch color TFT/LCD module:



4. Absolute Maximum Ratings

4.1 Absolute Ratings of TFT LCD Module

Item	Symbol	Min	Max	Unit	Conditions
Logic/LCD Drive Voltage	VDD	-0.3	+3.6	[Volt]	

4.2 Absolute Ratings of Environment

Item	Symbol	Min	Max	Unit
Operating Temperature	TOP	-30	+85	[°C]
Operation Humidity	HOP	5	90	[%RH]
Storage Temperature	TST	-30	+85	[°C]
Storage Humidity	HST	5	90	[%RH]

Note: Maximum Wet-Bulb should be 39°C and no condensation.

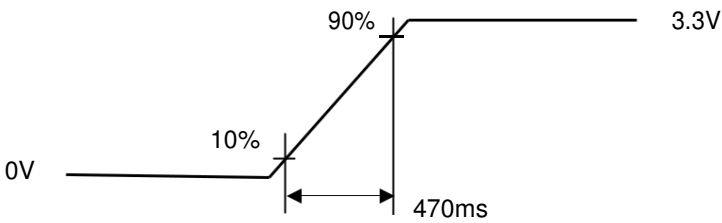
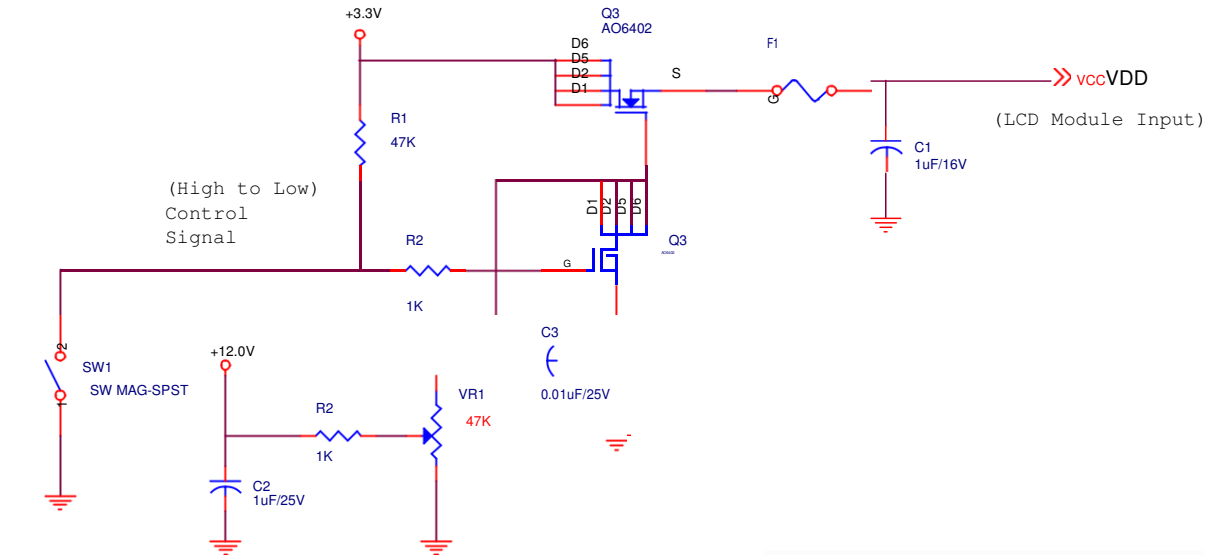
5. Electrical Characteristics

5.1 TFT LCD Module

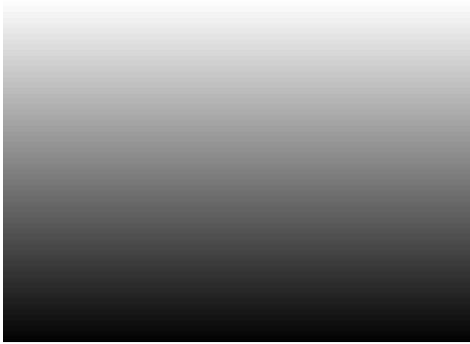
5.1.1 Power Specification

Symbol	Parameter	Min	Typ	Max	Units	Remark
VDD	Logic/LCD Drive Voltage	3.0	3.3	3.6	[Volt]	±10%
I _{VDD}	VDD Current	-	270	330	[mA]	64 Gray Bar Pattern (VDD=3.3V, at 60Hz)
I _{rush LCD}	LCD Inrush Current	-	-	3	[A]	Note 1
P _{VDD}	VDD Power	-	0.9	1.2	[Watt]	64 Gray Bar Pattern (VDD=3.3V, at 60Hz)

Note 1: Measurement condition:



VDD rising time



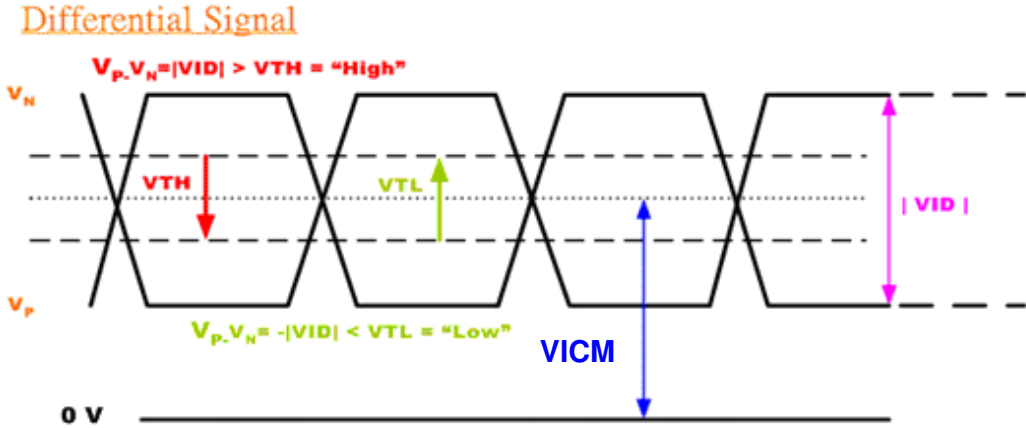
64 Gray pattern

5.1.2 Signal Electrical Characteristics

Input signals shall be low or Hi-Z state when VDD is off.

Symbol	Item	Min.	Typ.	Max.	Unit	Remark
VTH	Differential Input High Threshold	-	-	100	[mV]	VICM=1.2V
VTL	Differential Input Low Threshold	-100	-	-	[mV]	VICM=1.2V
VID	Input Differential Voltage	100	400	600	[mV]	
VICM	Differential Input Common Mode Voltage	1.1		1.6	[V]	VTH/VTL= ±100mV

Note: LVDS Signal Waveform.



5.2 Backlight Unit

5.2.1 Parameter guideline for LED backlight

Following characteristics are measured under a stable condition using an inverter at 25°C (Room Temperature):

Symbol	Parameter	Min.	Typ.	Max.	Unit	Remark
V _{CC}	Input Voltage	10.8	12	12.6	[Volt]	
I _{vcc}	Input Current	-	0.27	-	[A]	100% PWM Duty
P _{vcc}	Power Consumption	-	3.24	TBD	[Watt]	100% PWM Duty
I _{rush LED}	Inrush Current	-	-	TBD	[A]	at rising time=470us
F _{PWM}	Dimming Frequency	200	-	20K	[Hz]	
	Swing Voltage	3	3.3	5.5	V	
	Dimming Duty Cycle	TBD	-	100	%	
Operation Lifetime		50,000			Hrs	I _F =80mA, Ta= 25°C

Note 1: Ta means ambient temperature of TFT-LCD module.

Note 2: I_F means LED unit forward current.

Note 3: V_{CC}, I_{vcc}, P_{VCC}, I_{rush LED} are defined for LED B/L.(100% duty of PWM dimming)

Note 4: If HM084SV101A VER8 module is driven by high current or at high ambient temperature & humidity condition. The operating life will be reduced.

Note 5: Operating life means brightness goes down to 50% initial brightness. Minimum operating life time is estimated data.

6.2 Signal Description

LVDS is a differential signal technology for LCD interface and high speed data transfer device. The connector pin definition is as below.

Pin No.	Symbol	Description
1	VDD	Power Supply, 3.3V (typical)
2	VDD	Power Supply, 3.3V (typical)
3	UD	Vertical Reverse Scan Control, When UD=Low or NC → Normal Mode. When UD=High → Vertical Reverse Scan. <i>Note</i>
4	LR	Horizontal Reverse Scan Control, When LR=Low or NC → Normal Mode. When LR=High → Horizontal Reverse Scan. <i>Note</i>
5	RxIN1-	LVDS differential data input Pair 0
6	RxIN1+	
7	GND	Ground
8	RxIN2-	LVDS differential data input Pair 1
9	RxIN2+	
10	GND	Ground
11	RxIN3-	LVDS differential data input Pair 2
12	RxIN3+	
13	GND	Ground
14	RxCLKIN-	LVDS differential Clock input Pair
15	RxCLKIN+	
16	GND	Ground
17	SEL 68	LVDS 6/8 bit select function control, Low or NC → 6 Bit Input Mode. High → 8 Bit Input Mode. <i>Note</i>
18	NC	NC
19	RxIN4-	LVDS differential data input Pair 3. Must be tied to Ground in 6 bit input mode.
20	RxIN4+	

Note : “Low” stands for 0V. “High” stands for 3.3V. “NC” stands for “No Connected.”

6.3 Scanning Direction

The following figures show the image seen from the front view. The arrow indicates the direction of scan.



Fig. 1



Fig. 2



Fig. 3



Fig. 4

Fig. 1 Normal scan (Pin3, UD = Low or NC ; Pin4, RL = Low or NC)

Fig. 2 Reverse scan (Pin3, UD = Low or NC ; Pin4, RL = High)

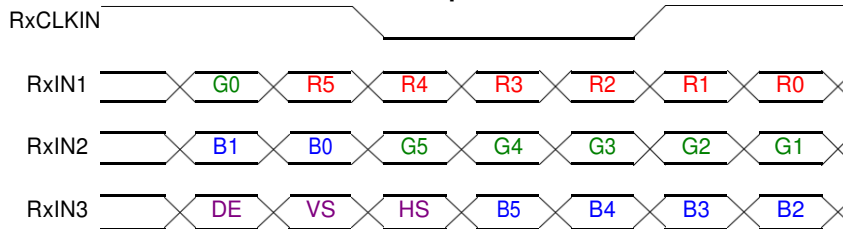
Fig. 3 Reverse scan (Pin3, UD = High ; Pin4, RL = Low or NC)

Fig. 4 Reverse scan (Pin3, UD = High ; Pin4, RL = High)

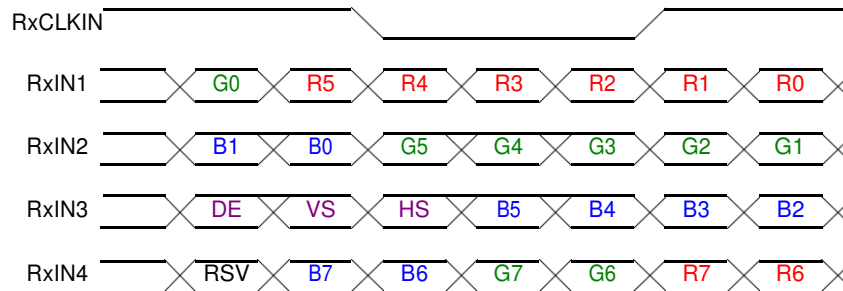
6.4 The Input Data Format

6.4.1 SEL68

SEL68 = "Low" or "NC" for 6 bits LVDS Input



SEL68 = "High" for 8 bits LVDS Input



Note1: Please follow PSWG.

Note2: R/G/B data 7:MSB, R/G/B data 0:LSB

Signal Name	Description	Remark
R7	Red Data 7 (MSB)	Red-pixel Data Each red pixel's brightness data consists of these 8 bits pixel data.
R6	Red Data 6	
R5	Red Data 5	
R4	Red Data 4	
R3	Red Data 3	
R2	Red Data 2	
R1	Red Data 1	
R0	Red Data 0 (LSB)	
G7	Green Data 7 (MSB)	Green-pixel Data Each green pixel's brightness data consists of these 8 bits pixel data.
G6	GreenData 6	
G5	GreenData 5	
G4	GreenData 4	
G3	GreenData 3	
G2	GreenData 2	
G1	GreenData 1	
G0	GreenData 0 (LSB)	
B7	Blue Data 7 (MSB)	Blue-pixel Data Each blue pixel's brightness data consists of these 8 bits pixel data.
B6	Blue Data 6	
B5	Blue Data 5	
B4	Blue Data 4	
B3	Blue Data 3	
B2	Blue Data 2	
B1	Blue Data 1	
B0	Blue Data 0 (LSB)	
RxCLKIN+	LVDS Clock Input	
RxCLKIN-		
DE	Display Enable	
VS	Vertical Sync	
HS	Horizontal Sync	

Note: Output signals from any system shall be low or Hi-Z state when VDD is off.

6.5 Interface Timing

6.5.1 Timing Characteristics

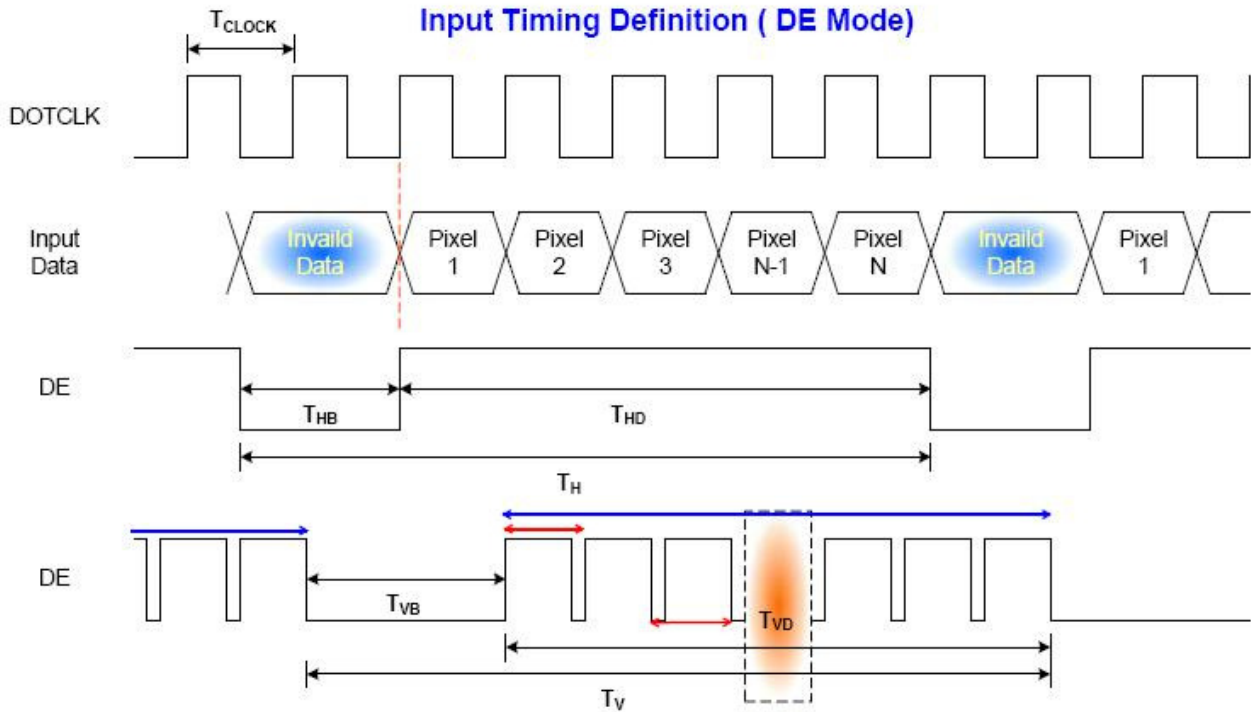
DE mode only

Parameter		Symbol	Min.	Typ.	Max.	Unit	Condition
Clock frequency		$1/T_{\text{Clock}}$	33.6	39.8	48.3	MHz	
Vertical Section	Period	T_V	608	628	650	T_H	
	Active	T_{VD}	600	600	600		
	Blanking	T_{VB}	8	28	50		
Horizontal Section	Period	T_H	920	1056	1240	T_{Clock}	
	Active	T_{HD}	800	800	800		
	Blanking	T_{HB}	120	256	440		

Note: Frame rate is 60 Hz.

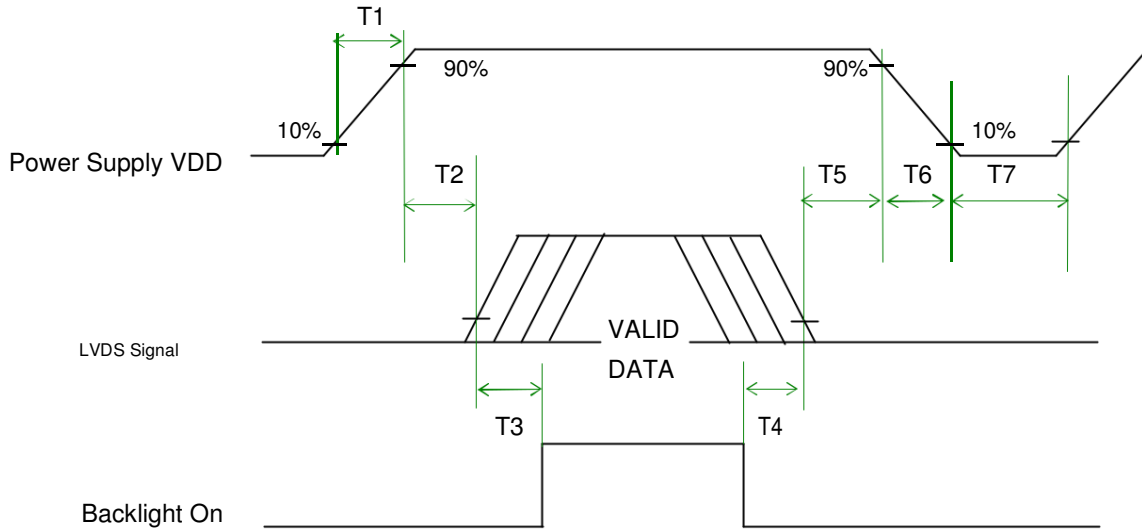
Note: DE mode.

6.5.2 Input Timing Diagram



6.6 Power ON/OFF Sequence

VDD power and lamp on/off sequence is as below. Interface signals are also shown in the chart. Signals from any system shall be Hi-Z state or low level when VDD is off.



Power ON/OFF sequence timing

Parameter	Value			Units
	Min.	Typ.	Max.	
T1	0.5	-	10	ms
T2	0	40	50	ms
T3	200	-	-	ms
T4	200	-	-	ms
T5	0	16	50	ms
T6	0	-	10	ms
T7	1000	-	-	ms

The above on/off sequence should be applied to avoid abnormal function in the display. Please make sure to turn off the power when you plug the cable into the input connector or pull the cable out of the connector.

7. Connector & Pin Assignment

Physical interface is described as for the connector on module. These connectors are capable of accommodating the following signals and will be following components.

7.1 TFT LCD Signal (CN1): LVDS Connector

Connector Name / Designation	Signal Connector
Manufacturer	STM
Connector Model Number	MSB24013P20HA or compatible.
Mating Model Number	STM-P24013P20

Pin No.	Signal Name	Pin No.	Signal Name
1	VDD	2	VDD
3	UD	4	LR
5	RxIN1-	6	RxIN1+
7	GND	8	RxIN2-
9	RxIN2+	10	GND
11	RxIN3-	12	RxIN3+
13	GND	14	RxCKIN-
15	RxCKIN+	16	GND
17	SEL 68	18	NC
19	RxIN4-	20	RxIN4+

7.2 LED Backlight Unit (CN2): LED Driver Connector

Connector Name / Designation	Lamp Connector
Manufacturer	Entery
Connector Model Number	ENTERY 3808K-F04N-02R or compatible.
Mating Model Number	H208K-P04N-02B

Pin #	Symbol	Pin Description
1	VCC	12V input
2	GND	GND
3	Display_ON/OFF	+3.3 V:ON, 0V:OFF
4	Dimming	PWM

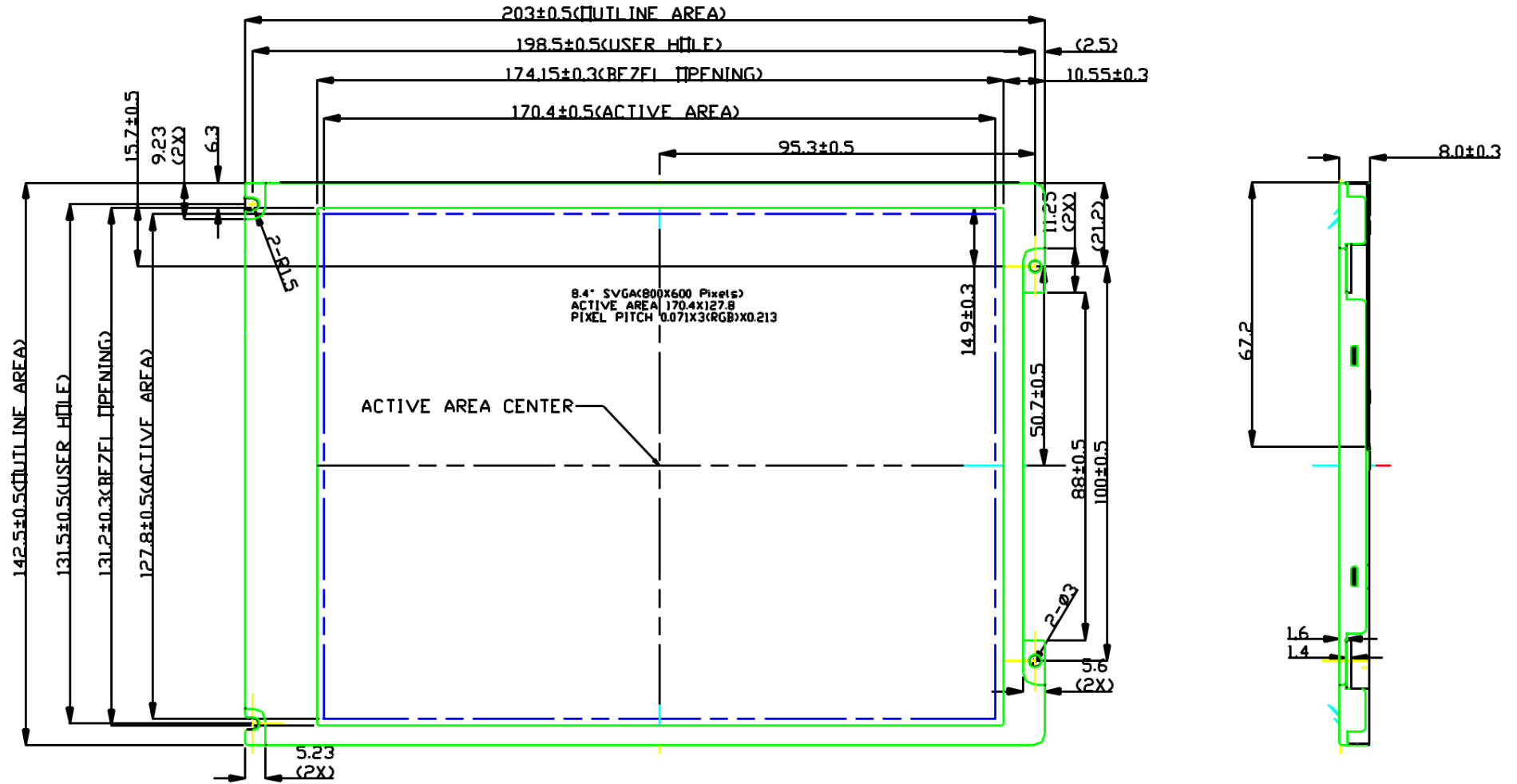
8. Reliability Test Criteria

Items	Required Condition	Note
Temperature Humidity Bias	40°C /90%,300 hours	
High Temperature Operation	85°C,300 hours	
Low Temperature Operation	-30°C,300 hours	
Hot Storage	85°C,300 hours	
Cold Storage	-30°C ,300 hours	
Thermal Shock Test	-20°C/30 min ,60°C/30 min ,100cycles	
Shock Test (Non-Operating)	50G,20ms,Half-sine wave,(±X, ±Y, ±Z)	
Vibration Test (Non-Operating)	1.5G, (10~200Hz, P-P) 30 mins/axis (X, Y, Z)	
On/off test	On/10 sec, Off/10 sec, 30,000 cycles	
ESD	Contact Discharge: ± 8KV, 150pF(330) 1sec, 8 points, 25 times/ point Air Discharge: ± 15KV, 150pF(330) 1sec, 8 points, 25 times/ point	Note 1

Note1: According to EN61000-4-2, ESD class B: Some performance degradation allowed. No data lost
. Self-recoverable. No hardware failures.

9. Mechanical Characteristics

9.1 LCM Front View



9.2 LCM Rear View



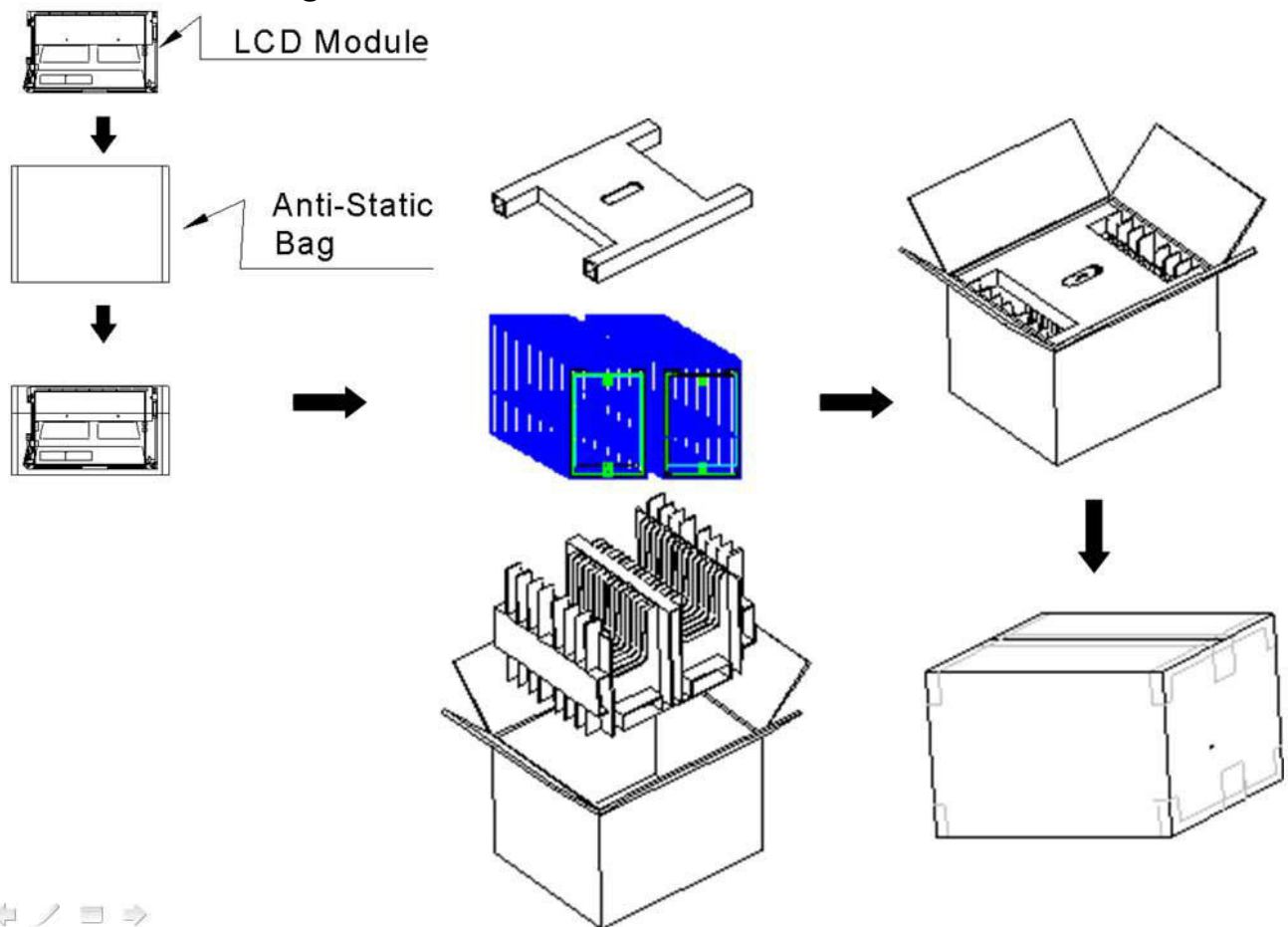
NOTE:
1. CONNECTOR:
CN1: STM MSB24013P20
MATING CONNECTOR:
STM P24013P20
CN2: ENTERY 3808K-F04N-02R
MATING CONNECTOR:
H208K-P04N-02B

10. Label and Packaging

10.1 Shipping Label (on the rear side of TFT-LCD display)



10.2 Carton Package



11 Safety

11.1 Sharp Edge Requirements

There will be no sharp edges or corners on the display assembly that could cause injury.

11.2 Materials

11.2.1 Toxicity

There will be no carcinogenic materials used anywhere in the display module. If toxic materials are used, they will be reviewed and approved by the responsible HIGHNESS toxicologist.

11.2.2 Flammability

All components including electrical components that do not meet the flammability grade UL94-V1 in the module will complete the flammability rating exception approval process.

The pRxINted circuit board will be made from material rated 94-V1 or better. The actual UL flammability rating will be pRxINted on the pRxINted circuit board.

11.3 Capacitors

If any polarized capacitors are used in the display assembly, provisions will be made to keep them from being inserted backwards.

11.4 National Test Lab Requirement

The display module will satisfy all requirements for compliance to:

UL 1950, First Edition

U.S.A. Information Technology Equipment